

## SPECIFICATION

## TITLE OF THE INVENTION

METHOD FOR ASSEMBLING INTEGRAL TYPE ELECTRONIC  
COMPONENT AND INTEGRAL TYPE ELECTRONIC COMPONENT

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## BACKGROUND OF THE INVENTION

10 The present invention relates to a method for assembling an integral type electronic component which is manufactured by combining a plurality of electronic components in one body, and an integral type electronic component assembled by the method.

15 In response to the recent advancement of making electronic components small and light-weight, there has been proposed a large number of ways to further miniaturize the electronic components. Many of the electronic components manufactured by the proposed ways are formed by combining an electronic component having two or more functions with a device having one function.

20 Various mounting processes and facilities have been developed hitherto to arrange the electronic components highly accurately with high reliability. A conventional method of assembling the electronic components will be described below with reference to Figs. 8-11.

25 In the first place, conductive adhesive 2 are supplied onto a board 1 as shown in Fig. 8. Then

electronic components 3 are placed to parts of the  
conductive adhesive 2 as shown in Fig. 9 and fixed to the  
conductive adhesive 2 with hardening the conductive  
adhesive 2. After conductive adhesive 4 are supplied onto  
5 the electronic components 3 as shown in Fig. 10, a board 5  
is placed on the conductive adhesive 4 as shown in Fig. 11.  
The conductive adhesive 4 are hardened last, whereby an  
integral type electronic component 6 is formed.

In the conventional arrangement of the above-  
10 described manner, the method requires mounting each of the  
components onto the board 1 and therefore is not fit for  
devices requiring a mounting accuracy, for example, in  
mounting optical components necessitating an optical path  
alignment, etc. The electronic components 3 undesirably  
15 vary in height from the board 1 because the electronic  
components 3 are placed on the conductive adhesive 2. As a  
result, when the board 5 is mounted onto the electronic  
components 3 in the next process, the so-called open fault  
that the electronic components 3 and the board 5 are not  
20 electrically connected with each other possibly arises.  
Further in the case where there are many electronic  
components 3 to be mounted, it takes a long time before all  
the components 3 are mounted completely, and also there are  
problems that a quality of mounting the components on the  
25 conductive adhesive 2 deteriorates and a cost increases

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a first board with a component storage part for

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In the second aspect, the electronic component may be held to the component storage part with a photocuring type insulating resin.

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enough simple to insert the electronic components to the component storage parts, a long time is not required to finish the mounting as compared with the conventional art even if a lot of electronic components are to be mounted.

5 The manufacturing time is shortened and costs can be reduced in comparison with the conventional art.

The electronic components can be arranged highly accurately and simply at low costs in comparison with the conventional art.

10 The so-called open fault can be avoided by flattening the bumps of the second board.

#### BRIEF DESCRIPTION OF THE DRAWINGS

15 These and other aspects and features of the present invention will become clear from the following description taken in conjunction with the preferred embodiments thereof with reference to the accompanying drawings, in which:

20 Fig. 1 is a sectional view of a first board among diagrams showing each of states of board and the like which is obtained by carrying out a method for assembling integral type electronic component according to an embodiment of the present invention;

25 Fig. 2 is a sectional view of the first board indicating a state with electronic components filled in

component storage parts of the first board shown in Fig. 1;

Fig. 3 is a sectional view of the first board indicating a state in which electronic components filled in the first board of Fig. 1 are fixed with an adhesive;

5 Fig. 4 is a side view of a second board to be fitted to the first board of Fig. 1;

Fig. 5 is a diagram showing a state in which the first board of Fig. 3 and the second board of Fig. 4 are joined;

10 Fig. 6 is a diagram of a state in which a third board is joined to the board obtained by joining the first board and the second board;

Fig. 7 is a flow chart explanatory of the method for assembling integral type electronic component in the embodiment of the present invention;

15 Fig. 8 is a diagram showing a state with a conductive adhesive supplied to a board among diagrams showing each of states of board and the like which is obtained by carrying out a conventional method for assembling integral type electronic component;

20 Fig. 9 is a diagram of a state with components mounted to the conductive adhesive of Fig. 8;

Fig. 10 is a diagram of a state with a conductive adhesive supplied to upper parts of components of Fig. 9; and

Fig. 11 is a diagram of a state with a board mounted to an upper part of the conductive adhesive of Fig. 10.

5 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

10 A method for assembling integral type electronic component, and an integral type electronic component assembled by the integral type electronic component assembling method which are embodiments of the present invention will be described below with reference to the drawings. Before the description of the present invention proceeds, it is to be noted that like parts are designated by like reference numerals.

15 The integral type electronic component assembling method is carried out in a manner as will be discussed below.

20 In a step (designated by "S" in Fig. 7) 1 of Fig. 7, component storage parts 102 are formed by dry etching or wet etching to a first board 101 as shown in Fig. 1. Each of the component storage parts 102 is a recessed part in which an electronic component 103 to be described below can be stored. Each component storage part may be a bottomed shape or can be, e.g., a through hole penetrating in a thickness direction of the first board 101 as indicated in  
25 Fig. 1. Although a plurality of the component storage

parts 102 are formed to the first board 101 in Fig. 1, there may be formed one storage part from a relationship with the electronic component 103 to be stored. The component storage part 102 is necessary to be larger by, for instance, 5-30 $\mu$ m than the electronic component 103 to be stored. For example, an Si substrate, a glass substrate, a ceramic substrate, an organic resin material substrate or the like is used as the first board 101.

A crystal orientation of Si may be any of (1,1,1), (1,0,0) and (1,1,0) when the Si substrate is used.

In a next step 2, as shown in Fig. 2, the electronic components 103 are disposed to the component storage parts 102. The electronic component 103 corresponds to, for example, a light-emitting element such as an LED (light-emitting diode) or the like, an IC of Si substrate, an IC of GaAs substrate, a resistor, a capacitor or the like. For the electronic component 103 being, e.g., the LED, an orientation of the LED is controlled by a mounting machine having a recognition function, so that the LED is stored in the component storage part 102 with a light-emitting part of the LED directed down in the drawing. Although the electronic components 103 are placed to all of the component storage parts 102 in Fig. 2, the present embodiment is not limited to this arrangement and the electronic component(s) 103 is sometimes placed to only



part of the component storage parts 102 in relation to a circuit design.

When the electronic component 103 is a light-emitting element such as an LED or the like, the component storage part 102 may be formed to have a side wall 1012 for shielding light of the light-emitting element. The side wall 1012 can prevent, for example, interference due to light emitted from an adjoining LED from arising.

In a succeeding step 3, as in Fig. 3, an insulating adhesive 104 is filled and then cured in the component storage part 102 with the electronic component 103 stored therein. An adhesive of a type cured with ultraviolet rays, a thermosetting type adhesive or the like can be used as an example of the insulating adhesive 104. When the adhesive to be cured with ultraviolet rays is used, for instance, the adhesive is cured by applying ultraviolet rays after the adhesive is filled.

In a next step 4, gold bumps 106 are formed correspondingly to the electronic components 103 to a second board 105 which is to be electrically connected to the electronic components 103. In a next step 5, it is decided whether or not leading end parts of the gold bumps 106 of the second board 105 are to be flattened. When the flattening is determined to be necessary, the step goes to a next step 6 and the flattening is carried out. Then the

step moves to a next step 7. On the other hand, when the flattening is determined to be unnecessary, the step skips to the step 7. Whether the flattening is necessary or not may be judged by a worker.

5 In the step 7 as shown in Fig. 4, a conductive adhesive 107 is applied to the leading end parts of the gold bumps 106 of the second board 105. The second board 105 may be a semiconductor chip of, e.g., Si, GaAs, InP or the like. In the step 7, the first board 101 with the electronic components 103 and the second board 105 with the gold bumps 106 are aligned to make the electronic components 103 and the gold bumps 106 meet each other, as indicated in Fig. 5. Then the first board 101 and the second board 105 are fitted. After the fitting, the conductive adhesive 107 is cured thereby joining the first board 101 and the second board 105. A first integral type electronic component 110 is formed in this manner.

According to the present embodiment, further in a step 8, a third board 108 is joined with a conductive adhesive 109 to the above-joined first board 101 and second board 105. A second integral type electronic component 111 may be formed accordingly as shown in Fig. 6.

The assembling method for the integral type electronic component, and the integral type electronic component assembled by the method can exert the following

effects. It has conventionally been the problem in the circumstances without the first board 101 how to assemble small components while fixedly transferring the components. That is, a process related to the arranging and fixing the components has been complicated in the conventional art, thereby raising problems of a yield decrease, etc. In contrast to this, according to the present embodiment, an arrangement accuracy of electronic components 103 is determined on the basis of an arrangement accuracy of component storage parts 102 formed to the first board 101. Moreover, the electronic components 103 stored in the component storage parts 102 are limited in motion. In order to arrange the electronic components 103, it is enough only to insert the electronic components 103 to the component storage parts 102. The arrangement is thus made simple. It won't take a long time before an end of the mounting as compared with the conventional art even if there is a large number of electronic components 103 to be mounted. The manufacturing time is shortened and costs can be reduced in comparison with the conventional art.

The arrangement of the electronic components 103 is realized highly accurately and simply at low costs in comparison with the conventional art.

Additionally, the open fault is prevented because the bumps 106 of the second board 105 are flattened.

Although the present invention has been fully described in connection with the preferred embodiment thereof with reference to the accompanying drawings, it is to be noted that various changes and modifications are apparent to those skilled in the art. Such changes and modifications are to be understood as included within the scope of the present invention as defined by the appended claims unless they depart therefrom.

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